

Fig. 1

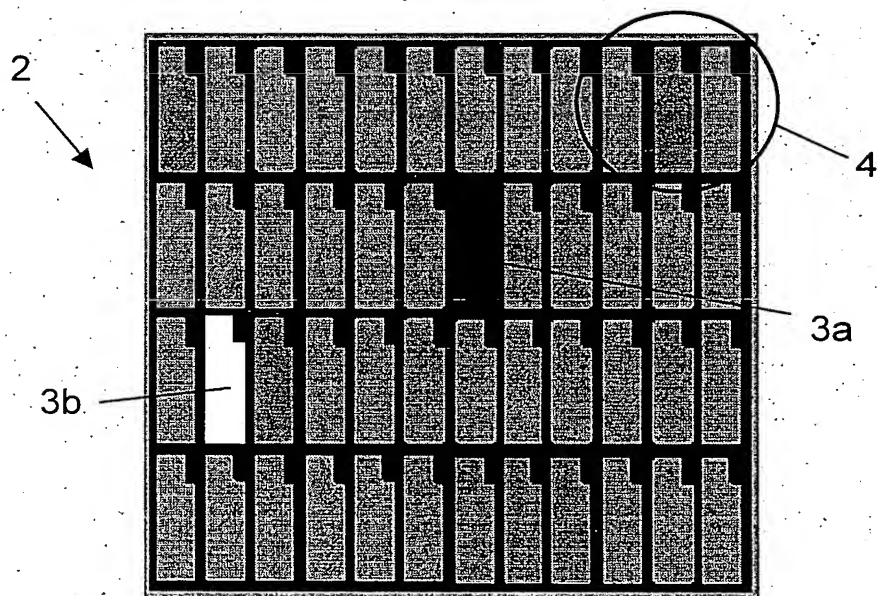


Fig. 2

BEST AVAILABLE COPY

Drive signals (row and column signals, power)

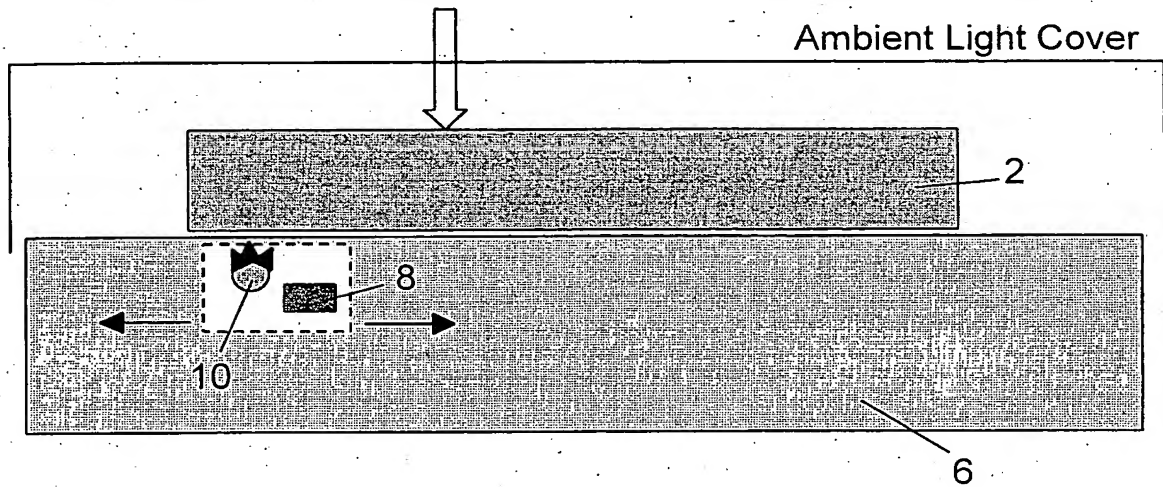


Fig. 3

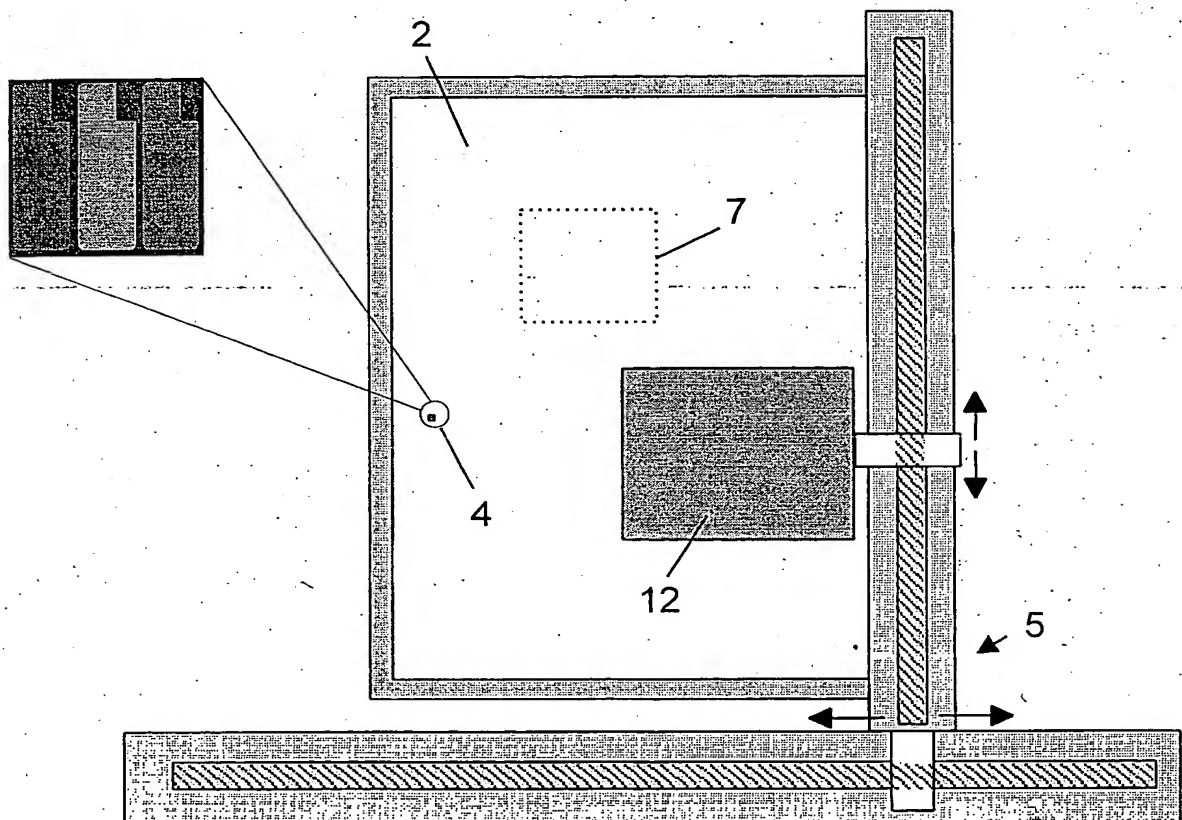
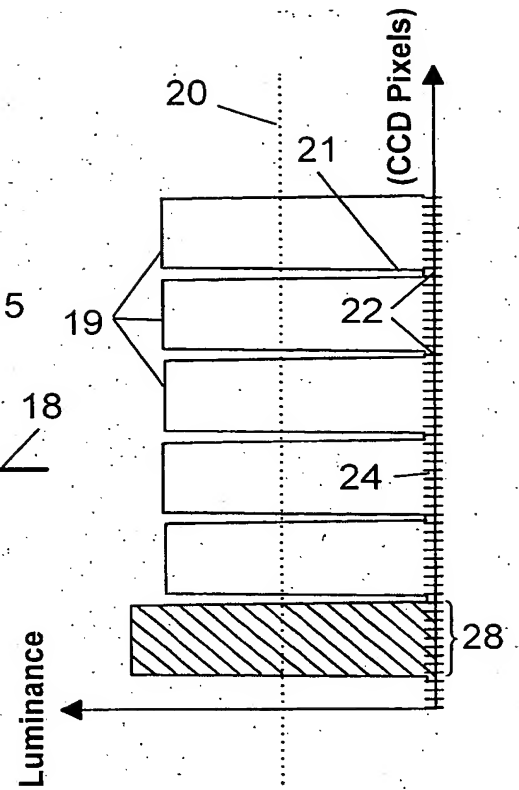
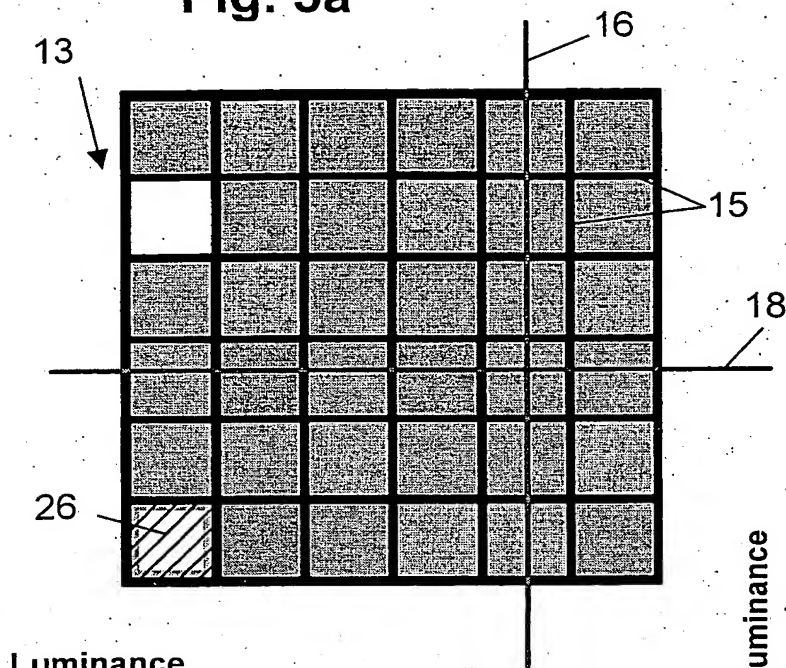
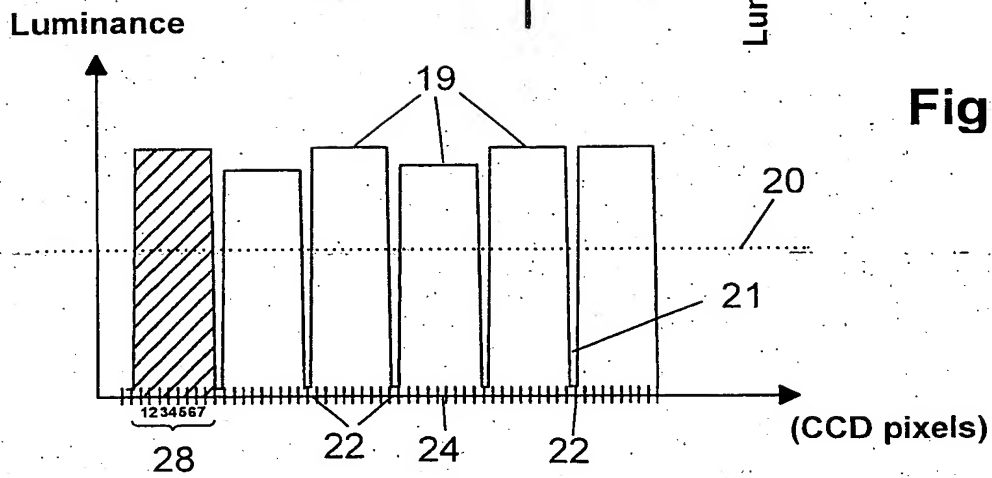
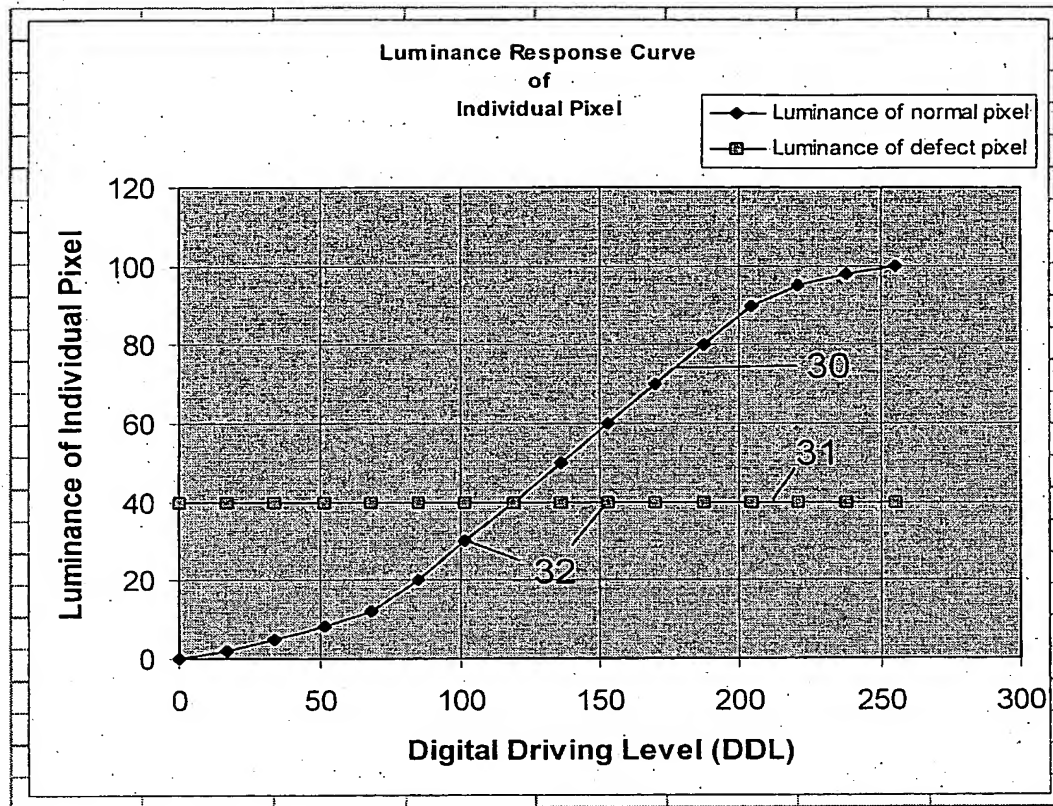


Fig. 4

Fig. 5a**Fig. 5c****Fig. 5b****Fig. 5**

**Fig. 6**

A	C	A	C	...
B	D	B	D	...
A	C	A	C	...
B	D	B	D	...
A	C	A	C	...
B	D	B	D	...
...

Fig. 7